



# A LOW COMPLEXITY OFDM MODULATOR AND DEMODULATOR **BASED ON DISCRETE HARTLEY TRANSFORM**

# otected by of ZAKARIA SEMBIRING (0830210304)

othisitemist

A thesis submitted in fulfilment of the requirements for the degree of **Master of Science (Communication Engineering)** 

## **School of Computer and Communication Engineering UNIVERSITI MALAYSIA PERLIS**

### 2011

## **UNIVERSITI MALAYSIA PERLIS**

	D	ECLARATION OF THESIS
Author's full name	:	ZAKARIA SEMBIRING
Date of birth	:	28 JANUARY 1970
Title	:	A LOW COMPLEXITY OFDM MODULATOR/DEMODULATOR BASED ON DISCRETE HARTLEY TRANSFORM
Academic Session	:	2009-2011
Academic Session       :       2009-2011         I hereby declare that the thesis becomes the property of Universiti Malaysia Perlis (UniMAP) and to be placed at the library of UniMAP. This thesis is classified as:         ONFIDENTIAL       {Contains confidential information under the Official Secret Act 1972}         RESTRICTED       {Contains restricted information as specified by the organization where research was done}         OPEN ACCESS       Lagree that my thesis is to be made immediately available as hard copy or on-line open access (full text)         I, the author, give permission to the UniMAP to reproduce this thesis in whole or in part for the purpose of research or academic exchange only (except during a period of years, if so requested above).		
		Certified by:
SIGNATURE       SIGNATURE OF SUPERVISOR         Zakaria Sembiring       Dr Mohd Fareq bin Abdul Malek		
IC.NO:B642663		
Date: 20 June 2	011	Date: 20 June 2011

othis temis protected by original copyright

othistemisprotected by original convieti

othistemisprotected by original copyright

othis temis protected by original copyright

#### ACKNOWLEDGMENT

First of all, I would like to thank to GOD for His Amazing Grace, blessing and guidance to complete a chapter of my life in term of finishing my research for 2 years in Universiti Malaysia Perlis, UniMAP.

In preparing this thesis, I have been helped by many people. In particular, I would like to express my deepest appreciation to my supervisor, Dr Mohd Fareq bin Abdul Malek for his invaluable guidance in the completion of this thesis. Also my thanks go to Puan Hasliza A Rahim, as my co supervisor for her availability to discuss about my research and problems and also for her suggestions and corrections of my thesis.

Special thanks to Assoc. Professor Dr R Badlishah Ahmad as Dean of School of Computer and Communication Engineering for his support and helpful to conduct my research here and also to Prof Nukala S Murthy for his supporting by his Grant in first time I begin my research.

For all of Professors, Dr and lecturers in school of Computer and Communication Engineering for their helpfulness in several ways as the part of important thing to finished my research. I am also very grateful to UniMAP for their support through MOSTI science fund.

I would like to thanks my wonderful family and relatives especially my wife Enda Yunita Surbakti for her love and patience to motivate me to finish my Master of Science in UniMAP. And for beloved children: Tasya Epifania Sembiring, Adrian Jordi Baptista Sembiring and Christopher Pasca Triananda Sembiring for their understanding, valuable motivation and praying during my studies here. I also would like to thank my uncle Prof. Dr. Ir Merdang Sembiring, for his supporting as my parent here. My thanks also goes to my friends in Embedded Computing Group, Zila, Farhan, Aja, Husna, Ezan, Nikman, Yacine, Mustafid, Naseem, Muthana, Thana, Layth, Ali, Hamzah, Ghasan and especially for my house mate : Iqmal Tahir, Wahyu Hidayat, Faisal Amri and Mohzani Khair Ishak for their friendship and helpful during that time.

And for all of others who have helped me in many other ways, thank you very much and God bless us forever.

othisitemisprotected by original convites

## TABLE OF CONTENTS

	Page
APPROVAL AND DECLARATION SHEET	ii
ACKNOWLEDGMENT	iii
TABLE OF CONTENTS	v
LIST OF FIGURES	ix
LIST OF TABLE	xii
LIST OF ABBREVIATIONS	xiii
ABSTRAK	xiv
ACKNOWLEDGMENT TABLE OF CONTENTS LIST OF FIGURES LIST OF TABLE LIST OF ABBREVIATIONS ABSTRAK ABSTRAK	XV
CHAPTER ONE INTRODUCTION	
Research Background	1
1.2 Problem Statement	3
1.3 Research Objectives	5
1.4 Research Scopes	5
1.5 Thesis Outline	7
1.6 Summary	8

## CHAPTER TWO LITERATURE REVIEW

2.1	Introduc	ction	10
2.2	Evolution of Multicarrier Modulation 10		10
2.3	Frequen	cy Division Multiplexing	11
2.4	Orthogo	onal Frequency Division Multiplexing (OFDM)	12
2.5	Digital	Generation Subcarriers	21
	2.5.1.	Discrete Fourier Transform vs. Discrete Hartley Transform	22
	2.5.2.	Fast Fourier Transform vs. Fast Hartley Transform	28
	2.5.3.	Digital to Analogue Conversion	40
	2.5.4.	Digital to Analogue Conversion Pulse Shaping Channel Propagation	40
	2.5.5.	Channel Propagation	41
	2.5.6.	AWGN Channel	43
	2.5.7.	Doppler Effect	46
	2.5.8.	Delay Spread	46
	2.5.9.	The Strength and Weakness of OFDM	48
2.6	Related	Work	52
2.7	2.7 Summary 57		
$\bigcirc$			

## CHAPTER THREE RESEARCH METHODOLOGY

3.1.	Introduction	59
3.2.	System Model Design	59
3.3.	Random Data Generator	63
3.4.	BPSK Constellation Mapping	64
3.5.	Inverse Discrete Transform	66

3.6.	Channel	Model	67
3.7.	Receiver	r Part	71
3.8.	Environ	ment and Key Indicator Performance Measurement	71
	3.8.1.	Environment	
	3.8.2.	Key Indicator Performance	
		3.8.2.1. BER versus SNR	73
		3.8.2.2. Operational Complexity	76
3.9.	OFDM S	Simulation Parameters	77
3.10.	. Summar	y opti	78
		Simulation Parameters	
СНАРТ	'ER FOU	IR RESULTS AND DISCUSSIONS	
4.1.	Introduc	tion	79
4.2.	System 1	Design Verification	80
	4.2.1. 0	Generation of OFDM Symbol Verification	80
	4	2.1.1. Random Data Generator	81
	· × °	A.2.1.2. BPSK Signal Mapping	82
	4	A.2.1.3. OFDM Symbol Generation	84

- 4.2.1.4. Cyclic Prefix Insertion 87
  - 4.2.1.6. Receiver Part 91

88

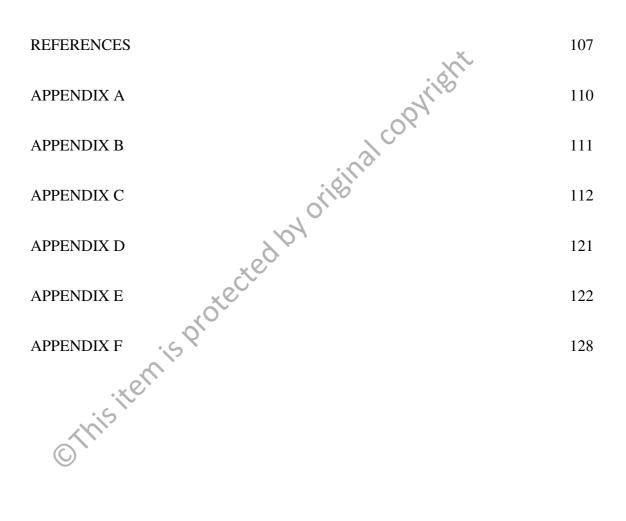
4.3. Gaussian Noise Tolerance 91

4.2.1.5. Channel AWGN

4.4. Effect of SNR Toward Quality of the Received Image
4.5. Complexity of System Design
4.6. Summary
103

#### CHAPTER FIVE CONCLUSIONS AND FUTURE WORK

5.1.	Conclusions	105
5.2.	Future Work	106



## LIST OF FIGURES

Figure 1.1	Internet traffic growth projected to 2011	2
Figure 2.1	Spectrum of MCM	14
Figure 2.2	Frequency division multiplexing: Analogue Transmitter	14
Figure 2.3	Time domain subcarriers within an OFDM symbol	16
Figure 2.4	Spectrum of an OFDM symbol with overlapping subcarriers	17
Figure 2.5	Frequency division multiplexing: Analogue Receiver	18
Figure 2.6	Block diagram of simplex point to point transmission using	19
	OFDM	
Figure 2.7	Conceptual Diagram of OFDM System Type1	20
Figure 2.8	Conceptual Diagram of OFDM system type 2	21
Figure 2.9	Data transported as a set of packets	23
Figure 2.10	OFDM subcarriers affected by a fading channel	25
Figure 2.11	Ideal filter at ADC the DAC	27
Figure 2.12	First step in the decimation in time algorithm	31
Figure 2.13	Three stages in the computation of an $N = 8$ point DFT	33
Figure 2.14	Eight-point decimation in time FFT algorithm	34

Figure 2.15 Basic butterfly computation in the decimation in time FFT algorithm 35

Figure 2.16	Use of an IFFT block to modulate an OFDM signal	35
Figure 2.17	Use of an FFT block to demodulate an OFDM signal	36
Figure 2.18	IFFT block and the frequency domain OFDM symbol at its output	38
Figure 2.19	OFDM symbols in a multipath channel	44
Figure 2.20	Time domain components of one subcarrier for two symbols showing cyclic prefix Example of an AWGN channel	44
	showing cyclic prefix	
Figure 2.21	Example of an AWGN channel	45
Figure 2.22	Signal PDF Voltage through AWGN	46
Figure 2.23	Taped delay line model on multipath fading	47
Figure 2.24	Multipath delay spread	49
-	The spectrum of orthogonal subcarriers	51
Figure 2.26	Conventional versus OFDM transmission	51
Figure 3.1	OFDM Model Used For Simulation	63
Figure 3.2	Flowchart of system design process	64
Figure 3.3	The source code used to generate the random binary data input for	65
	N = 64	
Figure 3.4	BPSK (a) Block diagram and (b) Constellation	67
Figure 3.5	Source code for BPSK signal mapping	67

Figure 3.6	The source code for generating OFDM symbol using IDHT and	69
	CP insertion	
Figure 3.7	Theoretical AWGN and Fading Channel graph	72
Figure 4.1	Random Binary Data as the input source for number of data = 64 bits	83
Figure 4.2	The output of BPSK constellation mapper	85
Figure 4.3	BPSK in constellation mapping plot (scatter plot)	85
Figure 4.4	OFDM signals as the output of IDHT in 3-D plot	88
Figure 4.5	The spectrum of OFDM signal for $N = 64$	88
Figure 4.6	The redisplayed spectrum of OFDM signal for $N = 10$ only	89
Figure 4.7	The signal transmitted in serial form	91
Figure 4.8	Signal after distort by AWGN for (a) SNR=5 and (b) SNR=10	92
Figure 4.9	The plotted graph of BER versus SNR for number of data = 1kbits	95
Figure 4.10	The plotted graph of deviation 1st data measurement BER vs. SNR	96
Figure 4.11	The plotted graph of average deviation data measurement vs. SNR	96
Figure 4.12	The plotted graph of BER versus SNR for number of data = 10 Kbits	97
Figure 4.13	The plotted graph of deviation data measurement of BER vs. SNR	98
Figure 4.14	The plotted graph of standard deviation data measurement vs. SNR	98
Figure 4.15	The effect of given value of SNR towards the quality of received	101
	image	

Figure 4.16 DHT matrices for N = 2,4 and 8

Figure 4.17 Plotted graph of cos(.), sin(.) and cas(.) 103

102

Figure 4.18 The complexity comparison operation between DFT versus DHT 104

in term of number of multiplication (N<sub>M</sub>) and number of addition

 $(N_A)$ 

Figure 4.19 The comparison complexity saving achievement of the 105

n of Michaeler M

## LIST OF TABLES

		Pages
Table 2.1	Typical delay spread	48
Table 3.1	Number of bits estimation used for AWGN simulation	69
Table 3.2	Number of bits estimation used to simulate Rayleigh fading	70
Table 3.3	OFDM system parameters	77
othis	Number of bits estimation used to simulate Rayleigh fading OFDM system parameters	

## LIST OF ABBREVIATIONS

ADC	Analog-to-Digital Converter
AWGN	Additive White Gaussian Noise
BER	Bit Error Rate
Bps	bits per second
BPSK	Binary Phase Shift Keying
BW	Bandwidth
СМ	Coefficient Memory
СР	Binary Phase Shift Keying Bandwidth Coefficient Memory Cyclic Prefix decibel Direct Current
dB	decibel
DC	Direct Current
DAC	Digital to Analog Converter
DFT	Discrete Fourier Transform
DHT	Discrete Hartley Transform
DIF	Decimation In Frequency
DIT JEL	Decimation In Time
DSL	Digital Subscriber Line
DSP	Digital Signal Processing
DVB-T	Digital Video Broadcasting-Terrestrial
FDM	Frequency Division Multiplexing
FFT	Fast Fourier Transform
FHT	Fast Hartley Transform
FIR	Finite Impulse Filter
IDFT	Inverse Discrete Fourier Transform
IDHT	Inverse Discrete Hartley Transform
IEEE	Institute of Electrical and Electronic Engineers

ICI	Intercarrier Interference
IFFT	Inverse Fast Fourier Transform
IFHT	Inverse Fast Hartley Transform
ISI	Intersymbol Interference
I/Q	In-phase/Quadrature
LAN	Local Area Network
LS	Least Square
LSB	Least Significant Bit
МСМ	Multicarrier Modulation
MSB	Least Significant Bit Multicarrier Modulation Most Significant Bit
OFDM	Orthogonal Frequency Division Multiplexing
OFDMA	Orthogonal Frequency Division Multiplexing Access
QAM	Quadrature Amplitude Modulation
QPSK	Quadrature Phase Shift Keying
SNR	Signal to Noise Ratio
WLAN	Wireless LAN
WiMAX	Worldwide Interoperability for Multiple Accesses
. Kell	
wimax othis tern	

#### SUATU SYSTEM MODULATOR DAN DEMODULATOR DENGAN KERUMITAN RENDAH BERBASIS DISCRETE HARTLEY TRANSFORM

#### ABSTRAK

Penyelidikan atas diskret Hartley (DHT) untuk menggantikan konvensional kompleks bernilai dan matang jelmaan Fourier diskret (DFT) sebagai modulator dan demodulator OFDM telah dijalankan dalam penyelidikan ini. Projek keseluruhan dibahagikan kepada dua bahagian seperti berikut: pertama, sistem yang dicadangkan adalah simulasi di bawah pelbagai parameter seperti BER berbanding SNR dan kualiti imej berbanding dengan nilai berkuat pasti SNR. Kedua, model simulasi telah dibangunkan menggunakan perisian yang sangat baik untuk simulasi, MATLAB. Analisis statistik telah disiasat untuk mengukur ketepatan prestasi sistem dan juga tingkat kerumitan sistem. Hasil daripada simulasi menunjukkan bahawa kerumitan sistem dikurangkan dalam hal jumlah proses darab dan prose penjumlahan. Untuk jumlah bilangan subcarriers, N = 64, 52% daripada pendaraban dan 28% daripada nombor penjumlahan telahpun dapat dikurangkan. Prestasi sistem diukur dari segi graf yang diplot daripada BER berbanding SNR dan kualiti imej yang diterima berbanding SNR, di mana nilai BER adalah 10<sup>-3</sup> dan kualiti imej adalah sama dengan imej asal apabila nilai tertentu othisitemisprotecte SNR dalam kisaran = 30 dB.

## A LOW COMPLEXITY OFDM MODULATOR AND DEMODULATOR BASED ON DISCRETE HARTLEY TRANSFORM

#### ABSTRACT

The investigation upon to discrete Hartley transform (DHT) to replace the conventional complex-valued and mature discrete Fourier transform (DFT) as the OFDM modulator and demodulator was carried out in this research. The overall project is divided into two parts as follows: firstly, the proposed system is simulated under various parameters such as BER versus SNR and the quality of image versus given value of SNR. Secondly, the simulation model has been developed using excellent software for simulation, MATLAB. Statistically analysis has been investigated to measure the accuracy of the system performance and also the complexity of the system. Simulation results show that the system complexity is reduced in term of multiplication and 28 % of addition number. For number of subcarriers, N = 64, 52 % of multiplication and 28 % of addition numbers are reduced. The system performance is measured in term of plotted graph of BER versus SNR and quality of received image versus SNR, where the BER value is  $10^{-3}$  and quality of image is similar with the original image when the given value of SNR = 30 dB.

#### **CHAPTER ONE**

#### **INTRODUCTION**

#### 1.1 Research Background

The rapid growth of Internet traffic has been placed the tremendous strain of conventional communication networks. This gains up is resulted by some new emerging applications, such as Internet video to TV and video communications. As the effect, the need of bandwidth becomes further increase up to triple the bandwidth demand by 2011. It does not appear that the growth of Internet traffic will slow in the foreseeable future as depicted by (Shieh & Djordjevic, 2010) in Figure 1.1. This phenomenal rise brings the changes of information infrastructure at every level, from core to metro and access networks. Therefore, the much more robustness broadband communication systems for supporting high data rate transmission are needed. Several technologies are proposed to cope with these challenges. One of the most promising technologies is orthogonal frequency division multiplexing (OFDM).

OFDM has emerged as the leading modulation/demodulation technique in the wired and wireless communication systems, because it can overcome the multipath fading effect and scale well even when the increase of data results in more severe intersymbol interference (ISI) (Tianhua Chen, 2010). As the special case of multicarrier modulation (MCM), OFDM is dedicated for supporting high speed data transmission.

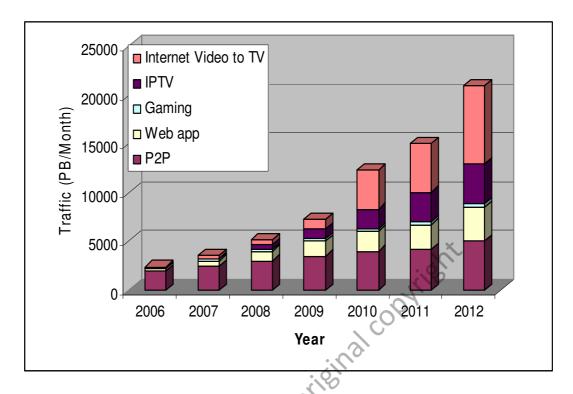


Figure 1.1: Internet traffic growth projected to 2011. (Modified from Cisco, Inc).

Moreover, this system is still capable to handle more services. This high capability is resulted from the allocating subcarriers overlapped one to another but there is no interference among them due to its orthogonality one to another. These features are then distinguished OFDM from the conventional frequency division multiplexing (FDM).

Most of OFDM transceiver employs inverse DFT (IDFT) and DFT to perform modulation and demodulation in transmitter and receiver, respectively. The system is then denoted as DFT-OFDM system. To improve the system performances, even further a fast computation algorithm, IFFT/FFT can be used. However, IFFT/FFT requires arithmetic complex-valued computations and complex multiplier in FFT core needs long-time and high complexity hardware design operational to enable high performance with stringent cost and power budget (Xilinx, 2010). For the current DFT-based OFDM transceivers, the modulator needs to compute a long-length inverse discrete Fourier transform (IDFT), and the demodulator needs to compute a long-length DFT, where the transform length is up to 512 or more. For such long-length IDFT/DFT computations, a huge numbers of complex multiplications are required and each of them basically involves four real multiplications and two real additions. Technically and economically, the larger number of arithmetic calculations, the higher cost, time and power consuming in it.

The complexity of a DFT-based OFDM transceiver will be reduced if corresponding modulator/demodulator is implemented using purely real-valued transformation. Therefore in this research, the design of OFDM modulator and demodulator are carried out to achieve the new system which has a lower complexity both in arithmetic calculation and hardware implementation. Moreover, with using realvalued signal, not only the number of arithmetic calculation can be reduced but also the applications of this new system is expected would be extended. One of the very suitable supporting applications is optical communication system as explained by (Armstrong, 2009) and (Shieh & Djordjevic, 2010).

#### **1.2 Problem Statement**

The focus of future fourth-generation (4G) mobile systems is to support high data rate services and to ensure seamless provisioning of services across a multitude of wireless systems and networks, from indoor to outdoor, from one air interface to another, and from private to public network infrastructure (Hara & Prasad, 2003). Moreover, OFDM hasbeen proposed to combine with optical communication system for supporting the high speed data communication system (Armstrong, 2009), (Shieh & Djordjevic, 2010).