SEMICONDUCTOR OPTICAL AMPLIFIER (SOA) LATTY SHAZANA BT ISMAIL FOR PHOTONIC SIGNAL PROCESSING

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APPROVAL AND DECLARATION SHEET

This report titled "Semiconductor optical amplifier (SOA) for photonic signal processing" is prepared and submitted by Latty Shazana Bt Ismail (081030299) and has been found satisfactory in term of scope, quality and presentation as partial fulfillment of the requirement for Bachelor of Engineering (Electronic Engineering) University Malaysia Perlis, (UniMAP).

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Semiconductor optical amplifier (SOA) for photonic signal processing.

ABSTRAK

Projek ini memaparkan ciri-ciri penguat optik semikonduktor dalam pemprosesan isyarat fotonik. Data- data yang berdasarkan ciri-ciri yang dianalisis adalah untuk aplikasi yang berpotensi dalam sistem fotonik seperti pensuisan dan penjana isyarat. Dengan menggunakan perisian Optisystem, ciri-ciri SOA dimodelkan dan mensimulasikan dengan memvariasikan parameter yang digunakan dalam SOA seperti arus, gandaan kuasa dan suhu. Kajian ini dapat membantu dalam merancang dan merekacipta aplikasi fotonik. Prestasi litar akan dapat dipertingkatkan dengan menggunakan parameter yang betul dan tepat dalam litar yang direkacipta. Dengan mengeksplorasi ciri-ciri SOA,ianya akan membantu perancang untuk memahami lebih lanjut tentang perilaku litar yang mereka perlukan untuk merekacipta litar menggunakan peranti SOA.

SEMICONDUCTOR OPTICAL AMPLIFIER (SOA) FOR PHOTONIC SIGNAL PROCESSING.

ABSTRACT

This project presents the characteristic of semiconductor optical amplifier in photonic signal processing. The characterization data is analyzed for potential application in photonic systems such as switching and signal generators. Using Optisystem, the characteristic is modeled and simulated by varying the parameter used in SOA such as current, gain and temperature. This characterization model will help in designing the application in photonic. Performance wise will be enhanced by employing correct parameters in the design. By exploring the properties, it will help the designer to understand more on the behavior of the circuit that they need to design using this SOA device.

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