-NL ARJUNA BIN MARZUKI DESIGN OF SUBSYSTEMS FOR MULTIBAND WIRELESS TRANSCEIVER

ARJUNA BIN MARZUKI

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DESIGN OF SUBSYSTEMS FOR MULTIBAND WIRELESS TRANSCEIVER

Sites the steel of In fulfilment of the requirements for the degree of

> **School of Microelectronic Engineering UNIVERSITI MALAYSIA PERLIS**

DECLARATION OF THESIS

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LIST OF SYMBOLS

	Ω	Ohm
	γ	Noise parameter, γ = 2/3 for long-channel
	δ	Coefficient of gate noise, $\delta = 2\gamma = 4/3$ for long-channel
	α	Noise parameter, $\alpha = g_m / g_{d0}$
	Γ	Reflection coefficient
	Γ_{S}	Reflection coefficient looking into the source
	Γ_{in}	Reflection coefficient looking into the input
	Γ_{L}	Reflection coefficient looking into the load
	Γ_{out}	Reflection coefficient looking into the output
	μ _n	Mobility of electron
	μ_{p}	Mobility of hole
	ξ	Noise parameter of the uncorrelated portion of the transistor's gate noise
	ξı	V_{DS} to V_{OV} ratio
	к	Noise parameter of the correlated portion of the transistor's gate noise
	x	Noise parameter that includes both correlated and uncorrelated portions of the transistor's gate noise
•	ρ	ho = V _{ov} /LE _{sat}
Ń	ε	Permittivity of free space, $\epsilon_0 = 8.854 \times 10^{-12}$ F/m
	λ	Wavelength of the frequency of operation
	A _d	Area drawn
	A _v	Voltage gain
	A _{vo}	Open-circuit voltage gain
	B _c	Correlation susceptance
	B _{opt}	Optimum susceptance
	B _S	Source noise susceptance

	B _{system}	System bandwidth
	С	Correlation coefficient, c = j0.395 for long-channel devices
	C _a	Areal capacitance
	C _c	Coupling capacitor
	C_{db}	Drain-Body capacitance
	C _f	Feedback capacitor
	C_{gd}	Gate-Drain capacitance
	C _{gs}	Gate-Source capacitance
	C_{gsn}	Gate-Source capacitance of NMOS
	C_{gsp}	Gate-Source capacitance of PMOS
	C_{gsT}	Total Gate-Source capacitance
	Clight	Speed of light, $c_{light} = 3 \times 10^8 \text{ m/s}$
	C _{ox}	Oxide capacitance of NMOS
	C _{oxn}	Oxide capacitance of PMOS
	C _{oxp}	Oxide capacitance
	C _p	Capacitance per unit periphery
	C _t	Total capacitance
	d	Largest dimension of the design
•	E	Average bit energy
	$(E_b/N_t)_{eff}$	Average bit energy to noise and interference power spectral density minimum ratio
	Ec	Average energy per PN chip
	$\overline{\mathbf{e}_{n}}$	External voltage noise generator
	E _{sat}	Field strength at which the carrier velocity has dropped to one half the value extrapolated at low-field mobility
	f	Frequency
	F	Noise factor
	f _{block}	Frequency of the block signal

	fcw	Spurious response frequencies
	f _{IF}	Frequency of the IF Signal
	\mathbf{f}_{LO}	Frequency of the LO Signal
	F _{min}	Minimum noise factor
	F _{min} °	Minimum noise factor for the classical noise matching input stage of the LNA
	f _{RF}	Frequency of the RF signal
	f _T	Transition frequency
	F _{uw}	Frequency of unwanted signal
	F _{UW1} (CW)	Frequency of the first unwanted signal of the CW nature
	F_{UW2} (Modulated)	Frequency of the second unwanted signal of the modulated nature
	f _{wanted}	Frequency of the wanted signal
	Gc	Correlation conductance
	g _{d0}	Drain-Source conductance at 0 V _{DS}
	G _f	Conductance of C _f
	g _g	Real, noiseless conductance in the gate circuit
	gm	Transconductance of the transistor
	G _m	Transconductance of the circuit
	g _{mb}	Body-effect transconductance of the MOSFET
	G _m -C	Transconductance-Capacitor
	G _{m_eff}	Effective transconductance of the circuit
	g _{mT}	Total transconductance
	G _n	Conductance contributing to thermal noise due to $\overline{i_{\rm n}^2}$
	G _{opt}	Optimum conductance
	G _s	Conductance contributing to thermal noise due to $\overline{i_s^2}$ or source conductance
	Iblocking (CW)	Blocking signal (CW) band power spectral density